



ADITYA ENGINEERING COLLEGE

An Autonomous Institution

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Aditya Nagar, ADB Road, Surampalem - 533437, Near Kakinada, E.G.Dt., Ph:99498 76662

Department of Electronics and Communication Engineering

M.Tech. (VLSI Design) - AR19 - Course Articulation Matrix

Note: Correlation Levels are 1 or 2 or 3. Where 1- Slight(Low), 2 - Moderate(Medium), 3 - Substantial (High).

	CO Statements	POs											PSOs		
I SEM															
Course Code	192VD1T01-CMOS ANALOG IC DESIGN	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Demonstrate the small signal and large signal models of CMOS transistors.	1	1	-	2	-	-	-	-	-	-	-	2	1	-
CO2	Analyze the characteristics of different CMOS circuits.	1	3	2	2	-	-	1	-	-	-	-	3	3	1
CO3	Develop the two stage CMOS operational amplifiers.	1	2	-	3	-	-	-	3	-	-	-	3	2	-
CO4	Analyze the basic comparator and different performance parameters of comparator.	2	3	2	-	-	-	-	-	-	-	-	3	3	1
CO5	Develop basic circuits based on the knowledge acquired in the course	2	3	3	-	3	-	3	3	-	-	-	3	3	-
Course Code	192VD1T02-CMOS DIGITAL IC DESIGN	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Explain the functionality of MOS inverters.	-	1	-	-	2	-	-	-	-	-	-	2	1	-
CO2	Analyze various combinational circuits designs in CMOS.	2	3	2	-	3	3	-	3	-	-	-	3	3	-
CO3	Analyze sequential logic gates designs in CMOS.	2	3	2	-	3	3	-	3	-	-	-	3	3	-
CO4	Explain the functionality of different arithmetic building blocks.	-	1	-	-	-	1	-	-	-	-	-	2	1	-
CO5	Analyze different semiconductor memories.	2	3	2	-	-	-	-	3	-	-	-	3	3	1

	CO Statements	POs											PSOs		
Course Code	192VD1E01-VLSI TECHNOLOGY (PROFESSIONAL ELECTIVE – I)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Summarize characteristics of MOS transistors.	3	2	-	-	-	-	-	-	-	-	-	3	2	-
CO2	Outline the MOS fabrication process and short channel effects.	3	2	2	-	-	-	-	-	-	-	-	3	2	-
CO3	Identify the basic rules in layout designing.	3	3	2	-	3	-	-	-	-	-	-	3	3	2
CO4	Analyze various combinational logic networks and sequential systems.	3	3	2	2	3	-	-	-	-	-	-	3	2	2
Course Code	192VD1E02-NANOMATERIALS AND NANOTECHNOLOGY (PROFESSIONAL ELECTIVE – I)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Understand the basic science behind the design and fabrication of nano scale systems.	1	1	-	2	-	-	-	3	-	-	-	2	1	-
CO2	Understand and formulate new engineering solutions for current problems and competing technologies for future applications.	1	3	2	2	-	-	1	-	-	-	-	3	3	1
CO3	Develop the inter disciplinary projects applicable to wide areas by clearing and fixing the boundaries in system development.	1	2	-	3	-	-	-	3	-	-	-	3	3	1
CO4	Dissect the operation of fabrication and characterization	2	3	2	-	-	-	-	-	-	-	-	3	3	3
CO5	Apply knowledge of science and Engineering	1	2	1	-	3	-	-	3	3	-	-	3	2	-
Course Code	192VD1E03-MEMS TECHNOLOGY (PROFESSIONAL ELECTIVE – I)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Summarise the basic concepts of MEMS technology and working of MEMS devices	1	-	-	-	-	-	-	-	-	-	-	1	-	1
CO2	Relate different materials for current MEMS devices and competing Technologies for future applications	-	1	-	-	-	-	-	-	-	-	-	1	-	1
CO3	Summarise the concepts of fabrication process of MEMS, Design and Packaging Methodology	-	1	1	-	-	-	-	-	-	-	-	1	-	1

	CO Statements	POs											PSOs		
CO4	Analyse the various fabrication techniques in the manufacturing of MEMS Devices	-	-	2	-	-	-	-	-	-	-	-	1	-	3
Course Code	192VD1E04-DEVICE MODELLING (PROFESSIONAL ELECTIVE – II)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Understand the physics of 2-terminal MOS operation and its characteristics.	-	1	-	2	2	-	-	-	-	-	-	2	1	-
CO2	Understand the physics of 4-terminal MOSFET operation and its characteristics.	-	1	-	-	2	-	-	-	-	-	-	2	1	-
CO3	Analyze the SOI MOSFET electrical characteristics.	-	3	-	-	3	-	-	-	-	-	-	3	3	1
CO4	Interpret various MOSFET models.	-	3	-	-	3	-	-	-	-	-	-	3	3	1
CO5	Analyse various high k dielectrics.	-	3	-	-	-	-	-	-	-	-	-	3	3	-
Course Code	192VD1E05-NANO-ELECTRONICS (PROFESSIONAL ELECTIVE – II)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Demonstrate challenges due to scaling on CMOS devices.	-	3	-	2	2	3	-	1	1	1	3	2	1	-
CO2	Analyse and explain working of novel MOS based silicon devices and various multi gate devices.	2	3	2	2	2	3	-	1	1	1	3	2	3	1
CO3	Analyse working of spin electronic devices	2	3	2	2	2	3	-	1	1	1	3	2	3	1
CO4	Summarize nano electronics systems and building blocks such as, low dimensional semiconductors, hetero structures, carbon nano tubes,	1	2	-	3	-	-	-	-	-	-	-	2	1	-
CO5	Develop nano electronics systems and building blocks such as: carbon nanotubes, quantum dots, nanowires etc.	-	2	-	3	3	2	-	2	2	2	2	3	2	-
CO6	Explain various design methodologies for chip design.	3	2	3	1	1	2	1	-	-	-	2	1	2	2
Course Code	192VD1E06-PHOTONICS (PROFESSIONAL ELECTIVE – II)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Classify the Optical sources and detectors and to discuss their principle.	2	1	-	-	-	-	-	1	-	-	-	2	1	-
CO2	Understand the Design considerations of fiber optic systems.	1	2	1	-	-	-	-	-	-	-	-	2	1	-
CO3	Analyse the basic methodologies behind the manufacturing of different LED materials.	2	3	1	-	-	-	-	-	-	-	-	2	3	-

	CO Statements	POs												PSOs		
CO4	Apply the principles of atomic physics to materials used in optics and photonics.	1	-	-	-	-	-	-	-	-	-	-	-	3	2	-
CO5	Use the tools, methodologies, language and conventions of physics to test and communicate ideas and explanations.	3	2	3	-	-	-	-	-	-	-	-	-	1	2	-
Course Code	192HS1T01-RESEARCH METHODOLOGY AND IPR	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3	
CO1	Understand research problem formulation.	3	2	-	-	-	-	-	-	-	-	-	-	-	-	-
CO2	Analyze research related information.	3	2	-	-	-	-	-	-	-	-	-	-	3	-	-
CO3	Demonstrate research ethics	2	1	-	-	-	2	-	2	-	-	-	-	2	3	-
CO4	Explain the today's world is controlled by Computer, Information Technology, but tomorrow world will be ruled by ideas, concept, and creativity.	-	-	-	-	-	2	3	-	-	-	-	-	2	2	-
CO5	Discuss that when IPR would take such important place in growth of individuals & nation, it is needless to emphasize the need of information about Intellectual Property Right to be promoted among students in general & engineering in particular.	-	-	-	-	-	-	-	3	-	-	-	-	3	3	-
CO6	Understand that IPR protection provides an incentive to inventors for further research work and investment in R & D, which leads to creation of new and better products, and in turn brings about, economic growth and social benefits.	-	-	-	-	-	-	-	3	-	-	-	-	3	-	-
Course Code	192VD1L01-CMOS ANALOG IC DESIGN LAB	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3	
CO1	Make use of the CAD Tool in modeling CMOS analog circuits.	-	2	-	1	3	-	-	-	-	-	-	-	3	-	-
CO2	Outline the circuit behavior based on different input conditions.	-	1	-	-	-	-	-	-	3	3	-	-	3	2	-
CO3	Interpret the electrical parameters of CMOS analog circuits.	-	-	-	3	-	-	-	-	3	3	-	-	3	-	-
CO4	Examine the Layout diagrams that are drawn by using EDA Tools.	-	-	-	2	3	-	-	-	-	-	-	-	3	-	3
Course Code	192VD1L02-CMOS DIGITAL IC DESIGN LAB	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3	
CO1	Identify different VLSI Design Methodologies using Mentor Graphics Tools.	-	2	1	3	-	-	-	-	-	-	-	-	1	1	3
CO2	Compare schematics for all digital designs and implement using simulation tools.	3	3	3	3	-	-	-	-	-	-	-	-	3	3	3

	CO Statements	POs										PSOs			
CO3	Discover layouts physically through various Back end EDA Tools.	-	-	2	3	3	3	-	-	3	-	-	2	2	3
CO4	Examine the performance extracted layouts through DRC, LVS, and PEX.	3	-	-	-	-	-	-	-	3	3	3	3	3	3
CO5	Evaluate the performance of combinational and sequential designs for its speed and other performance parameters.	3	3	3	3	-	3	2	-	-	-	-	3	3	3

II SEM

	CO Statements	POs											PSOs		
Course Code	192VD2E07-DESIGN FOR TESTABILITY (PROFESSIONAL ELECTIVE – III)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Demonstrate advanced knowledge in the basic faults that occur in digital systems, Testing of stuck at faults for digital circuits, Design for testability.	-	1	-	-	-	-	-	-	-	-	-	2	1	-
CO2	Analyse testing issues in the field of digital system design critically for conducting research.	2	3	2	-	-	3	-	-	-	-	-	3	3	-
CO3	Solve engineering problems by modelling different faults for fault free simulation in digital circuits.	-	-	3	-	-	-	3	-	-	-	-	3	-	3
CO4	Apply appropriate research methodologies and techniques to develop new testing strategies for digital and mixed signal circuits and systems.	1	2	1	-	-	2	-	-	-	-	-	3	2	-
Course Code	192VD2E08-IoT AND ITS APPLICATIONS (PROFESSIONAL ELECTIVE – III)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Explain the various technologies in IoT Architectures and IoT Platforms.	3	1	-	-	-	-	-	-	-	-	-	2	-	-
CO2	Apply Different Layer Protocols for building an IoT Network for the Applications.	1	2	-	-	-	-	-	-	-	-	-	3	-	-
CO3	Develop an IoT Applications using appropriate software's and hardware's.	1	2	1	-	-	-	-	-	-	-	-	3	2	-
CO4	Analyze the Data flow in the IoT Networks with appropriate frameworks.	-	-	2	-	3	-	-	-	-	-	-	-	3	1
CO5	Analyze the real time IoT Applications with different case studies.	-	3	2	-	3	-	-	-	-	-	-	3	3	1
Course Code	192VD2E09-VLSI SIGNAL PROCESSING (PROFESSIONAL ELECTIVE – III)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Explain parallel and pipelining processing techniques.	2	3	3	-	3	3	-	3	-	-	-	2	-	-
CO2	Identify applications for unfolding algorithm.	3	1	-	-	2	-	-	-	-	-	-	3	-	-
CO3	Analyse Systolic Design for Space Representations containing Delays.	3	3	2	-	-	-	-	-	-	-	-	3	-	-
CO4	Explain Cook-Toom Algorithm, Fast Convolution algorithm by Inspection method.	2	1	1	-	2	-	-	-	-	-	-	2	-	-
CO5	Analyse Power Reduction techniques and Power Estimation techniques.	3	3	2	-	-	-	-	3	-	-	-	4	-	1

	CO Statements	POs											PSOs		
Course Code	192VD2E10-MICROCONTROLLERS AND PROGRAMMABLE DIGITAL SIGNAL PROCESSORS (PROFESSIONAL ELECTIVE – IV)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Develop the moderate complex programs for embedded applications.	1	-	1	-	-	-	-	-	-	-	-	-	2	-
CO2	Compare the different ARM processor core based SoC with several features/ peripherals based on requirements of embedded applications.	2	3	-	-	-	-	-	-	-	-	-	2	-	-
CO3	Analyze the architectures in Programmable DSP processors.	1	-	1	-	-	-	-	-	-	-	-	-	-	-
CO4	Distinguish the different types of TMS320C6000 family	2	-	-	-	-	-	-	-	-	-	-	3	-	-
CO5	Develop digital signal processing applications using code composer studio.	-	-	-	2	-	-	-	-	-	-	-	2	-	-
Course Code	192EM2E11-NETWORK SECURITY & CRYPTOGRAPHY (PROFESSIONAL ELECTIVE – IV)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Explain basic concepts of encryption techniques.	-	1	-	2	2	1	-	3	3	-	-	2	1	-
CO2	Identify and utilize different forms of cryptography techniques	1	2	1	3	3	2	-	3	3	-	-	3	2	1
CO3	Illustrate authentication requirements with algorithms.	-	1	-	2	2	1	-	3	3	-	-	2	1	-
CO4	Apply authentication and security in the network applications	1	2	1	3	3	2	-	3	3	-	-	3	2	-
CO5	Compare different types of threats to the system and handle the same	-	1	-	2	2	1	-	3	3	-	-	2	1	-
Course Code	192VD2E11-LOW POWER VLSI DESIGN (PROFESSIONAL ELECTIVE – IV)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Make use of the concepts of Low-Power Design Approaches in complex systems design.	3	2	1	-	-	-	-	-	-	-	-	3	-	2
CO2	Contrast different Low-Voltage Low-Power design approaches for a target design.	-	3	-	2	3	-	-	-	-	-	-	3	-	-
CO3	Inspect the low power methods to get the circuits for reduced power dissipation.	-	-	2	2	-	-	-	-	-	-	-	3	-	3
CO4	Choose the suitable design among the list of Low-Voltage Low-Power Adders or Multipliers.	-	-	-	3	3	-	-	-	-	-	-	3	-	3

	CO Statements	POs											PSOs		
CO5	Extend the knowledge of power management strategies in power efficient VLSI design.	-	1	-	-	2	-	-	-	-	-	-	2	-	1
Course Code	192VDL03-MIXED SIGNAL IC DESIGN LAB	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Design the compensated op-amp with resistor and miller compensation.	3	3	3	3	3	3	-	-	-	-	-	3	3	-
CO2	Explain the operation of comparator through the two stage cross coupled clamped comparator.	-	1	-	2	2	1	-	-	-	-	-	2	1	-
CO3	Design the switched capacitor circuit using parasitic sensitive integrator and parasitic insensitive integrator	3	3	3	3	3	3	-	-	-	-	-	3	3	-
CO4	Demonstrate the operation of PLL and VCO through its design.	-	1	-	2	2	1	-	-	-	-	-	2	1	-
CO5	Explain the operation layouts of all the circuits that are designed and simulated.	-	1	-	2	2	1	-	-	-	-	-	2	1	-
Course Code	192VD2L04-PHYSICAL DESIGN AUTOMATION LAB	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Contrast the fluency in designing Algorithms.	2	-	2	3	-	-	-	-	-	-	-	3	3	1
CO2	Create and run scripts using C, C++ Programming Language.	2	3	2	-	3	-	-	-	-	-	-	3	3	1
CO3	Analyze methods of Floor Planning & Routing Algorithms.	-	1	-	2	2	-	-	-	-	-	-	2	1	-
III SEM															
Course Code	192VD3E12-SCRIPTING LANGUAGES FOR VLSI (PROFESSIONAL ELECTIVE – V)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Contrast the fluency in programming with scripting languages.	2	-	2	3	-	-	1	-	-	-	3	3	3	1
CO2	Create and run scripts using PERL/ TCL/ PYTHON in CAD Tools.	2	3	2	-	3	-	1	-	-	-	-	3	3	1
CO3	Identify the use of PERL/PYTHON/ TCL in developing system and web applications.	-	1	-	2	2	1	-	3	-	-	1	2	1	-
CO4	Analyze methods for Libraries and Packages.	1	2	-	3	-	-	-	-	-	-	-	3	2	-

	CO Statements	POs											PSOs		
CO3	Interpret the shear strength of cohesive and cohesionless soils.	1	2	2	3	1	-	-	-	-	-	-	-	-	-
CO4	Analyse a shallow foundation for a given soil condition and loading.	-	3	3	3	3	1	-	-	-	-	-	-	-	-
CO5	Analyse a deep foundation for a given loading and soil conditions.	-	3	3	3	2	1	-	-	-	-	-	-	-	-
Course Code	192PD3O01-RENEWABLE ENERGY TECHNOLOGIES (OPEN ELECTIVE)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Identify alternate energy sources.	1		-	2		-	-	-	-	-	2	-	-	-
CO2	Analyze and design induction generator for power generation from wind.	-		-		-	-	-	-	-	3	-	-	-	-
CO3	Analyze different wind power plants.	2	3	3	3		-	-	-	2	-	-	-	-	-
CO4	Design MPPT controller for solar power utilization.	3		-		2	-	-	-	-	-	-	-	-	-
CO5	Illustrate the basic operation of fuel cells	1	2	-	-	-	-	-	-	-	-	-	-	-	-
Course Code	192PD3O02-HYBRID ELECTRIC VEHICLES (OPEN ELECTIVE)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Illustrate the performance characteristics of converter fed DC drives.	2	1	2	-	-	-	-	-	1	-	-	-	-	-
CO2	Analyze the two quadrants and four quadrant controls of DC motor drives.	2	3	3	-	-	-	-	-	1	-	-	-	-	-
CO3	Develop the mathematical models of DC drive components	1	2	3	2	-	-	-	-	-	-	-	-	-	-
CO4	Analyze the four quadrant and closed loop control of DC-DC converter fed DC drive.	2	3	3	3	1	-	-	1	2	-	-	-	-	-
CO5	Propose various controlling techniques of DC drives for industrial applications.	3	3	3	3	3	-	-	3	3	3	-	-	-	-
CO6	Design various power electronic converters to control the DC motors	3	3	3	3	3	-	-	3	3	3	-	-	-	-
Course Code	192PD3O03-ENERGY AUDIT AND CONSERVATION AND MANAGEMENT (OPEN ELECTIVE)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Explain energy efficiency, conservation and various technologies.	2	1	-	-	2	2	3	2	3	3	2	-	-	-

	CO Statements	POs											PSOs		
Course Code	192TE3O03-GREEN ENGINEERING TECHNOLOGY (OPEN ELECTIVE)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Distinguish the various solar energy collection methods and measuring instruments.	3	1	-	3	-	-	-	-	3	2	-	-	-	-
CO2	Explain the different methods of solar energy storage and their applications	3	3	1	3	-	-	1	-	3	3	-	-	-	-
CO3	Illustrate the various types of wind mills and performance characteristics.	3	3	2	1	-	-	1	-	2	2	-	-	-	-
CO4	Explain the principle of Biomass production, Geothermal energy sources and Ocean thermal energy conversion	3	3	2	1	-	-	1	-	2	2	-	-	-	-
CO5	Illustrate the various types of electrical systems and mechanical systems.	1	2	1	1	-	-	1	-	1	1	-	-	-	-
CO6	Compare the various energy efficient process	2	1	1	1	-	-	2	-	2	2	-	-	-	-
Course Code	192TE3O04-IC ENGINES (OPEN ELECTIVE)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Predict the engine combustion characteristics.	3	3	2	3	1	3	-	-	-	-	-	-	-	-
CO2	Evaluate engine performance	3	3	2	3	1	3	-	-	-	-	-	-	-	-
CO3	Interpret the formation of engine emission and their control strategies	3	3	2	3	1	3	-	-	-	-	-	-	-	-
CO4	Distinguish the usage of different alternative fuels and their compatibility with fossil fuels	3	3	3	3	1	3	-	-	-	-	-	-	-	-
CO5	Explain the constructional and working principles of electrical vehicle and their accessories	1	2	1	1	1	3	-	-	-	-	-	-	-	-
Course Code	192TE3O05-AUTOMOTIVE TECHNOLOGY (OPEN ELECTIVE)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Summarize the vehicle chassis layout and constructional features of vehicle body.	3	2	1	-	-	-	-	-	1	1	-	-	-	-
CO2	Explain the constructional and working principles of sprung masses	2	1	1	1	-	-	-	-	-	1	-	-	-	-
CO3	Explain the constructional and working principles of unsprung masses.	3	2	1	1	-	-	-	-	-	1	-	-	-	-

	CO Statements	POs											PSOs		
Course Code	CO Statements	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO2	Outline the MOS fabrication process and short channel effects.	3	2	2	-	-	-	-	-	-	-	-	-	-	-
CO3	Identify the basic rules in layout designing	3	3	2	-	3	-	-	-	-	-	-	-	-	-
CO4	Analyze various combinational logic networks and sequential systems	3	3	2	2	3	-	-	-	-	-	-	-	-	-
Course Code	192VD3O03-NANO-ELECTRONICS (OPEN ELECTIVE)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Demonstrate challenges due to scaling on CMOS devices	-	3	-	2	2	3	-	1	1	1	3	-	-	-
CO2	Analyse and explain working of novel MOS based silicon devices and various multi gate devices	2	3	2	2	2	3	-	1	1	1	3	-	-	-
CO3	Analyse working of spin electronic devices	2	3	2	2	2	3	-	1	1	1	3	-	-	-
CO4	Summarize nano electronics systems and building blocks such as: low dimensional semiconductors, hetero structures, carbon nano tubes, quantum dots, nanowires etc.	1	2	-	3	-	-	-	-	-	-	-	-	-	-
CO5	Develop nano electronics systems and building blocks such as: carbon nanotubes, quantum dots, nanowires etc.	-	2	-	3	3	2	-	2	2	2	2	-	-	-
CO6	Explain various design methodologies for chip design.	3	2	3	1	1	2	1	-	-	-	2	-	-	-
Course Code	192CS3O01-PYTHON PROGRAMMING (OPEN ELECTIVE)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3
CO1	Apply fundamental concepts of Python programming language.	3	3	-	-	3	-	-	-	-	-	-	-	-	-
CO2	Develop programs using control statements.	3	2	-	-	-	-	-	-	-	-	-	-	-	-
CO3	Use data structures in Python to solve various problems.	-	2	1	1	-	-	-	-	-	-	3	-	-	-
CO4	Develop programs using functions, strings and files.	3	-	-	-	3	-	-	-	-	-	-	-	-	-
CO5	Make Use of Standard libraries like math, turtle, tkinter, re etc. in building real time applications.	-	-	-	1	3	-	-	-	-	-	3	-	-	-
CO6	Discuss on Object Oriented Programming concepts and Exceptions.	-	1	-	-	2	-	-	-	-	-	2	-	-	-
CO7	Design various applications using database connectivity.	3	2	1	1	3	-	-	-	-	-	3	-	-	-

	CO Statements	POs												PSOs		
CO4	Explain the principal involved in gathering oil and gas storage.	3	2	-	1	1	-	-	-	-	-	-	-	-	-	-
CO5	Understand the basic concepts of Downstream processing.	2	2	-	2	1	-	1	-	-	1	1	-	-	-	-
Course Code	192PE3O02-PROCESS INTENSIFICATION (OPEN ELECTIVE)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3	
CO1	Apply the basic principles and mechanisms that are responsible for process intensification.	3	2	1	2	-	-	-	-	-	-	-	-	-	-	-
CO2	Analyze various modifications to process equipment and designs with which process intensification becomes a reality in unit operations and unit processes	2	2	1	2	-	1	-	-	-	-	-	-	-	-	-
CO3	Analyze various case studies available in petrochemical, fine chemical, bioprocesses and carbon capture	3	2	1	2	-	-	-	-	-	-	-	-	-	-	-
CO4	Correlate textbook reported methodologies with Computational Fluid Dynamics.	3	2	-	1	1	-	-	-	-	-	-	-	-	-	-
CO5	Correlate textbook reported methodologies with experimental process intensification.	2	2	-	2	1	-	1	-	-	1	1	-	-	-	-
Course Code	192PE3O03-FUNDAMENTALS OF LIQUEFIED NATURAL GAS (OPEN ELECTIVE)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3	
CO1	Explain the LNG value chain.	1	1	-	-	-	-	-	-	-	-	-	-	-	-	-
CO2	Classify the different liquefaction technologies of LNG.	3	2	-	-	1	-	-	-	-	-	-	-	-	-	-
CO3	Explain the components of LNG receiving terminals.	3	-	-	-	-	-	-	-	-	-	-	-	-	-	-
CO4	Summarize LNG storage and transportation facilities	3	1	-	-	-	-	-	-	-	-	-	-	-	-	-
CO5	Identify major equipment and safety aspects of LNG industry.	3	2	-	-	3	-	-	-	-	-	-	-	-	-	-
Course Code	192PE3O04-SUBSEA ENGINEERING (OPEN ELECTIVE)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2	PSO3	
CO1	Explain Overall View of subsea engineering.	-	3	-	-	-	-	-	-	-	-	-	-	-	-	-
CO2	Explain the Subsea Distribution System	-	3	-	-	-	-	2	1	-	-	-	-	-	-	-

